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June 9, 2004

Time: 1:52 PM
(Santa Clara, California)

To: Office of Initial Patent Examination

From: Pamela J. Squyres
Attorney Docket: MA-002-1-I-b
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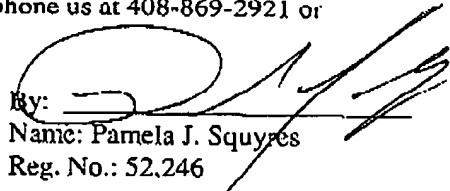
Re: Patent Application of: N. Johan Knall et al.	Examiner:
Serial No.: 10/805,147	Group Art Unit: 2818
Filed: March 19, 2004	Attorney Docket No.: MA-002-1-I-b
Title: Three Dimensional Memory Array and Method of Fabrication	

Document(s) Transmitted:

- ☐ Request for Correction (1 pg.)
- ☐ Filing Receipt (1 pg.)

Total pages of this transmission, including the cover letter: 3

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By: 
Name: Pamela J. Squyres
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): N. Johan Knall et al.

Application No.: 10/805147

Filed: 3/19/2004

Title: Three Dimensional Memory Array and
Method of Fabrication

Attorney Docket No.: MA-002-1-I-b

Art Unit:

2818

Examiner:

Unknown

REQUEST FOR CORRECTION

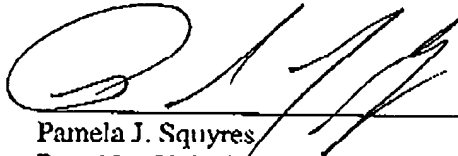
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Sir:

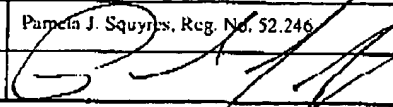
Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon.

Respectfully submitted,

6/9/04
Date


Pamela J. Squyres
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/805,147	03/19/2004	2818	770	MA-002-1-I-b	8	9	1

MATRIX SEMICONDUCTOR
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CONFIRMATION NO. 8647

FILING RECEIPT



OC000000012853480

Date Mailed: 06/03/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

N. Johan Knall; Sunnyvale, CA;
Mark Johnson, Los Altos, CA;

CORRECT info:

10/689,187 10/20/03

INCORRECT ✓

Domestic Priority data as claimed by applicant

This application is a CON of 10/687,187 10/16/2003
which is a CON of 10/153,999 05/22/2002 PAT 6,653,712
which is a DIV of 09/814,727 03/21/2001 PAT 6,420,215
which is a CIP of 09/560,626 04/28/2000 ABN

Foreign Applications

If Required, Foreign Filing License Granted: 06/02/2004

Projected Publication Date: 09/09/2004

Non-Publication Request: No

Early Publication Request: No

Title

Three-dimensional memory array and method of fabrication

